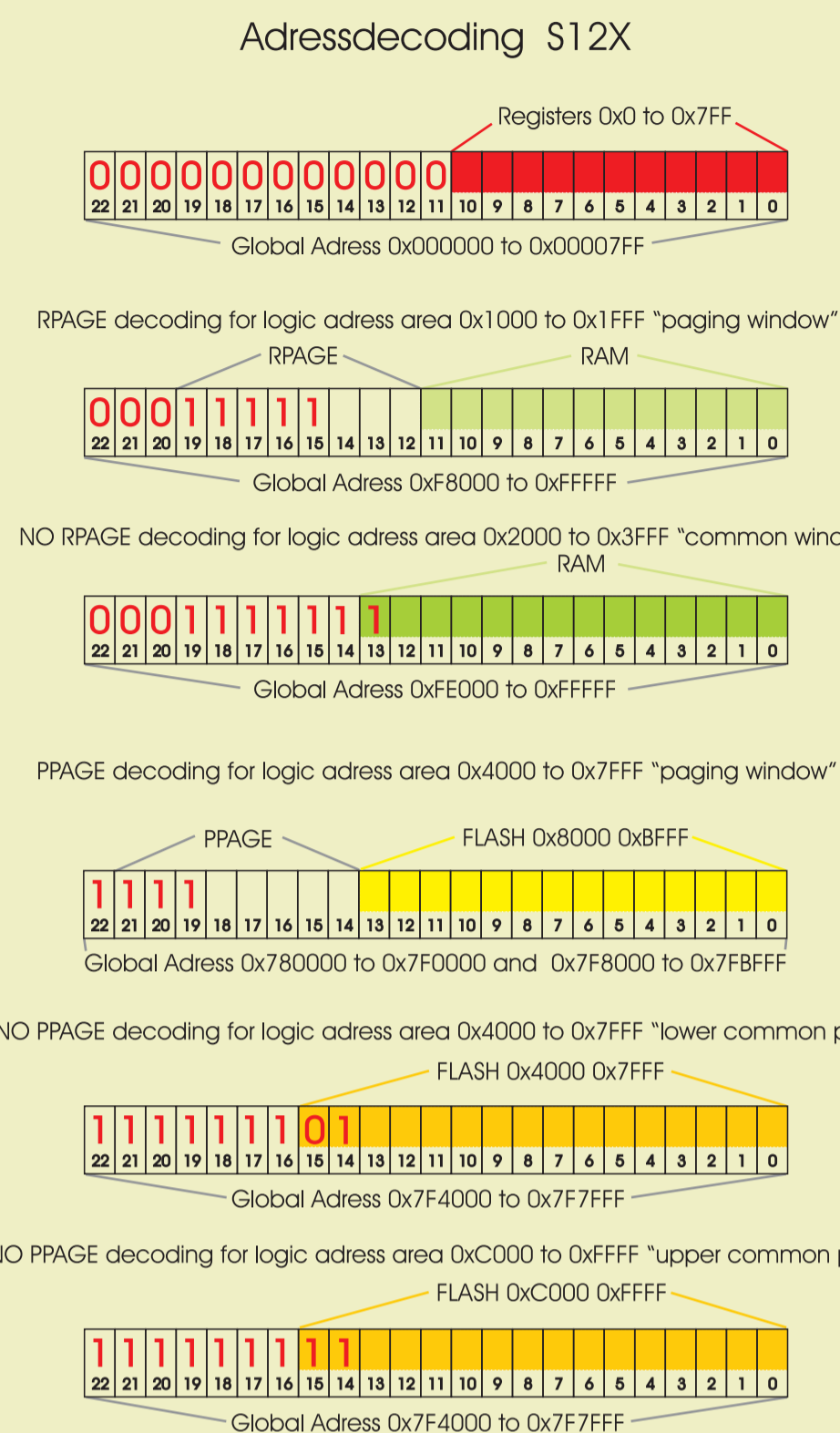
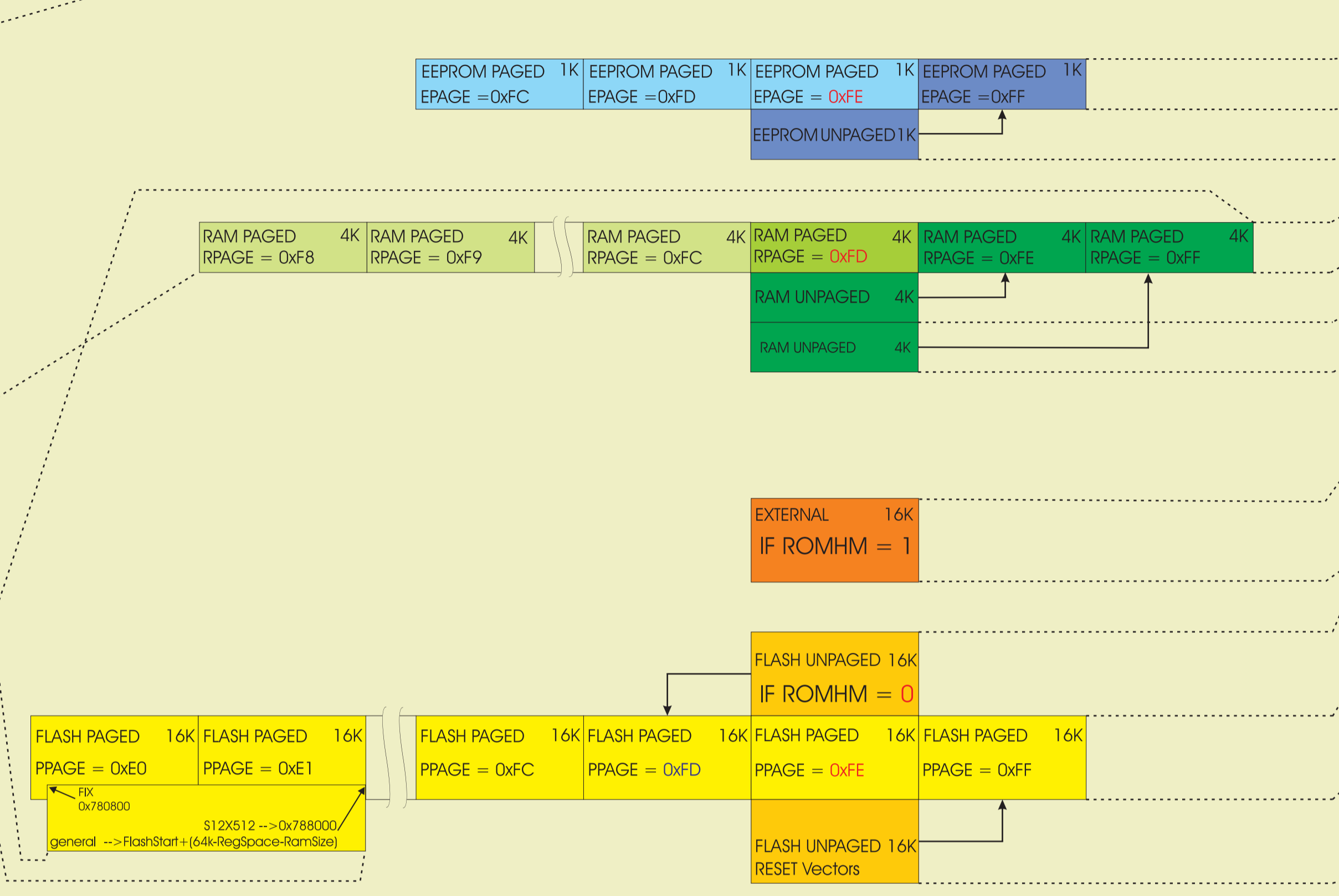
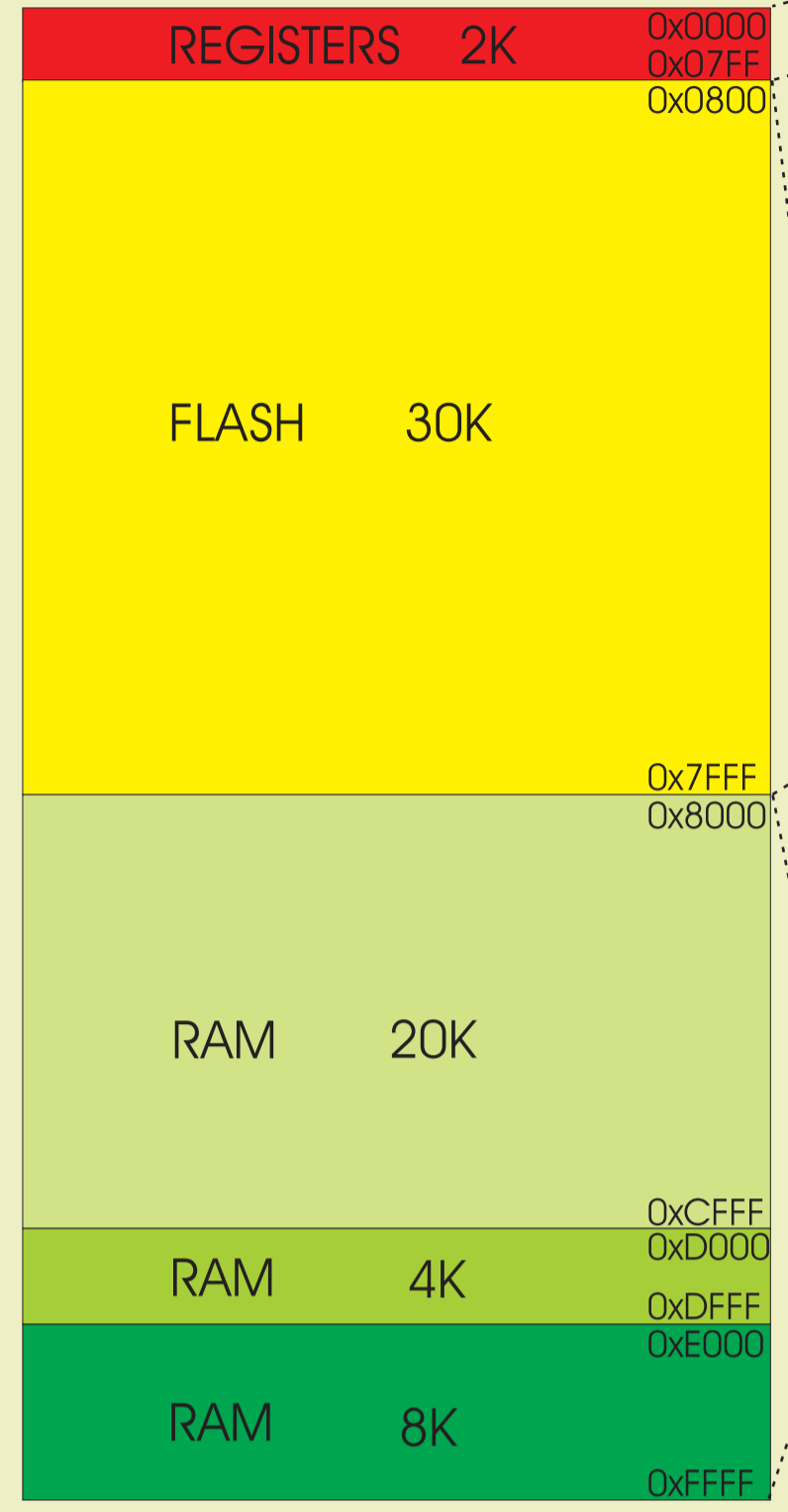
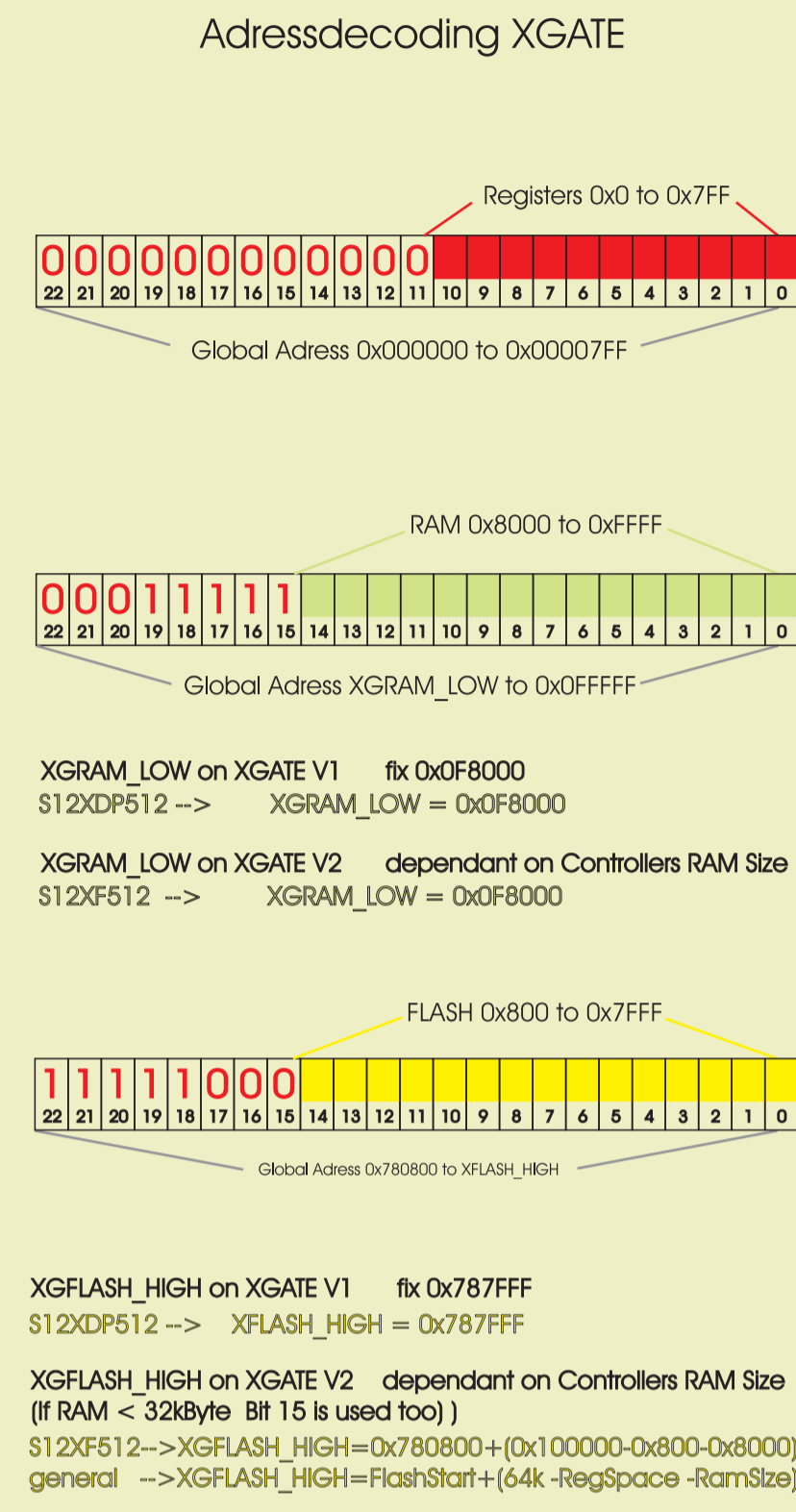




XGATE Core MAP

S12x Core paging MAP

S12x Core logical MAP



MC9S12XDP512 COSMIC Linker Segment Control Options			
Addressing Details for FLASH / EEPROM / RAM paging Access		GLOBAL MAP	Addressing Details for Global Access
logical	global		global
-b 0x000000		REGISTERS	-b 0x000000 -ds 16
	0x000800 0x001000	ext. RPAGE RAM access available if EEPROM switched off max. addressable Space 253 * 4K	
-o 0x1000	-b 0x0F8000	RAM PAGED RPAGE = 0xF8	-b 0x0F8000 -ds 16
-o 0x2000	-b 0x0FE000	RAM UNPAGED RPAGE = 0xFD RPAGE = 0xFE	-b 0x0FE000 -ds 16
	0x010000	max. addressable Space 253 * 1K	
-o 0x0800	-b 0x13F000	EEPROM PAGED EPAGE = 0xFC EPAGE = 0xFE	-b 0x13F000 -ds 16
-o 0x0C00	-b 0x13FC00	EEPROM UNPAGED EPAGE = 0xFF	-b 0x13FC00 -ds 16
-o 0x4000	-b 0x144000	EXTERNAL IF ROMHM = 1 16K	-b 0x144000 -ds 16
	0x40000	max. addressable Space 253 * 16K	
-o 0x8000	-b 0x780000	FLASH PAGED 29*16K PPAGE = 0xE0	-b 0x780000 -ds 16
-o 0x4000	-b 0x7F4000	FLASH UNPAGED 16K PPAGE = 0xFC PPAGE = 0xFD	-b 0x7F4000 -ds 16
-o 0x8000	-b 0x7F8000	FLASH PAGED 16K PPAGE = 0xFE	-b 0x7F8000 -ds 16
-o 0xC000	-b 0x7FC000	FLASH UNPAGED 16K PPAGE = 0xFF	-b 0x7FC000 -ds 16

Memory Model without PAGING	
all Page Registers fix on Default Values	
FLASH 48k	EEPROM 2k
ABSOLUTE PLACEMENT in I/O Header File using @0x_absolute_address	
.data .bss	-o 0x1000 logical Start Address -b 0x0FD000 physical Start Address -m 0x3000 max size
.eeprom	-o 0x0800 logical Start Address -b 0x13F000 physical Start Address -m 0x1000 max size
.text .const	-o 0x4000 logical Start Address -b 0x7F4000 physical Start Address -ds 0 if -bs0 on Command Line not used

Memory Model for Code/Data PAGING	
FLASH 512k	
ABSOLUTE PLACEMENT in I/O Header File using @0x_absolute_address	
.fdata	-o 0x1000 logical Start Address -b 0x0F8000 physical Start Address -ds 12 auto fill window size 4k -m 0x6000 maximum space 6*4k
.feprom	-o 0x0800 logical Start Address -b 0x13F000 physical Start Address -m 0x400 window 1k
.fdata .bss	-o 0x2000 logical Start Address -b 0x0FE000 physical Start Address
.fconst	-o 0x8000 logical Start Address -b 0x780000 physical Start Address -w 0x4000 auto fill window size 16k -m 0x74000 max space 28*16k
.fconst	-o 0x4000 logical Start Address -b 0x7F4000 physical Start Address
.fconst	-o 0x8000 logical Start Address -b 0x7F8000 physical Start Address
.fconst	-o 0xC000 logical Start Address -b 0x7FC000 physical Start Address

